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PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant(s): Lawrence Spracklen et al.
Title: EXECUTION DISPLACEMENT READ-WRITE ALIAS PREDICTION
Application No.: 10/822,390 Filed: April 12, 2004
Examiner: Eddie P. Chan Group Art Unit: 2183
Atty. Docket No.: 004-40364 Confirmation No.: 2873

December 27, 2005

Mail Stop Amendment
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37 C.F.R. § 1.97(b)

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- ☐ Form(s) PTO/SB/08A (N/A page(s), with copy of no reference(s)).
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- ☐ Listing of Pending Applications (N/A page(s), with copy of no application(s)).
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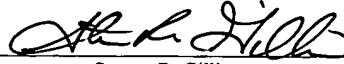
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- ☐ Pursuant to 1287 OG 163 (October 19, 2004), for those cited pending U.S. patent applications filed (or which entered the national stage under 35 U.S.C. § 371) on or after June 30, 2003 or which are otherwise stored in the USPTO's Image File Wrapper system, copies are not included.

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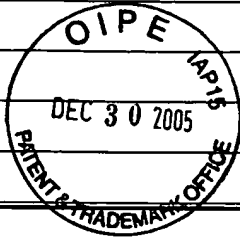
Respectfully submitted,



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| PTO/SB/08B (Substitute for form 1449/PTO) | | Attorney Docket No.: 004-40364 |
| | | Application No.: 10/822,390 |
| INFORMATION DISCLOSURE STATEMENT BY APPLICANT | | First Named Inventor: Lawrence Spracklen |
| | | Filing Date: April 12, 2004 |
| | | Group Art Unit: 2183 |
| | | Examiner Name: Eddie P. Chan |
| Sheet 1 of 1 | | Date Submitted: December 27, 2005 |



NON PATENT LITERATURE DOCUMENTS

| Examiner Initials* | Cite No. ¹ | Include name of author (in CAPITAL LETTERS), title of article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published. | T ² |
|--------------------|-----------------------|---|----------------|
| | | RAJWAR, Ravi, "Speculation-Based Techniques for Lock-Free Execution of Lock-Based Programs," PhD Dissertation, School of Computer Sciences, University of Wisconsin, Madison, WI, 2002, pages 1-224. | |
| | | STEFFAN, J. Gregory, "Hardware Support for Thread-Level Speculation," PhD Thesis, School of Computer Science, Carnegie Mellon University, April 2003, pages 1-161. | |
| | | STEFFAN, J. Gregory et al., "A Scalable Approach to Thread-Level Speculation," <i>ACM SIGARCH Computer Architecture News</i> , Volume 28, Issue 2, May 2000, 12 pages. | |
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